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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10067424	FILING DATE 02/07/2002	CLASS 257	SUBCLASS 1003	GAU 2815	EXAMINER Brook
**APPLICANTS: Lu Jeng; Mei Ping; Lemmi Francesco; Street Robert; Boyce James;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/419,293 10/15/1999					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 7447.0021-01	
TITLE : Dual dielectric structure for suppressing lateral leakage current in high fill factor arrays <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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